

10/728,645

EAST History

2/3/05

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| BRS | 6357 | multiplexer adj circuit | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB |
| BRS | 343 | S1 and (look-up adj tables) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB |
| BRS | 423 | S1 and (look adj up adj tables) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB |
| BRS | 423 | S2 or S3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB |
| BRS | 62 | S4 and pld | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB |
| BRS | 79 | S4 and (programmable adj logic adj device) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB |
| BRS | 82 | S5 or S6 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB |